



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



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華凌光電股份有限公司

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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF101NTYA4LNGO#

<p>APPROVED BY:</p> <p>(FOR CUSTOMER USE ONLY)</p>	<p>PCB VERSION: _____</p> <p>DATA: _____</p>
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭
ISSUED DATE: 2023/07/14			

TFT Display Inspection Specification: <https://www.winstar.com.tw/technology/download.html>

Precaution in use of TFT module: <https://www.winstar.com.tw/technology/download/declaration.html>

RECORDS OF REVISION

DOC. FIRST ISSUE

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2023/05/05		First issue
A	2023/06/13		Add NTSC Ratio Modify Power Sequence
B	2023/07/14		Modify Driver IC.

Contents

- 1.Module Classification Information
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- 3.General Specifications
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- 7.Power Sequence
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- 9.Interface
- 10.Reliability
- 11.Touch Panel Information
- 12.Contour Drawing
- 13.Other

1.Module Classification Information

W F 101 N T Y A 4 L N G 0 #
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪ ⑫ ⑬

①	Brand : WINSTAR DISPLAY CORPORATION											
②	Display Type : F→TFT Type, J→Custom TFT											
③	Display Size : 10.1” TFT											
④	Model serials no.											
⑤	Backlight Type :	F→CCFL, White S→LED, High Light White					T→LED, White Z→Nichia LED, White					
⑥	LCD Polarize Type/ Temperature range/ Gray Scale Inversion Direction	A→Transmissive, N.T, IPS TFT C→Transmissive, N. T, 6:00 ; F→Transmissive, N.T,12:00 ; I→Transmissive, W. T, 6:00 K→Transflective, W.T,12:00 L→Transmissive, W.T,12:00 N→Transmissive, Super W.T, 6:00					Q→Transmissive, Super W.T, 12:00 R→Transmissive, Super W.T, O-TFT V→Transmissive, Super W.T, VA TFT W→Transmissive, Super W.T, IPS TFT X→Transmissive, W.T, VA TFT Y→Transmissive, W.T, IPS TFT Z→Transmissive, W.T, O-TFT					
⑦	A : TFT LCD B : TFT+SCREW HOLES+CONTROL BOARD C : TFT+ SCREW HOLES +A/D BOARD D : TFT+ SCREW HOLES +A/D BOARD+CONTROL BOARD E : TFT+ SCREW HOLES +POWER BOARD					F : TFT+CONTROL BOARD G : TFT+ SCREW HOLES H : TFT+D/V BOARD I : TFT+ SCREW HOLES +D/V BOARD J : TFT+POWER BD						
⑧	Resolution:											
	A	128160	B	320234	C	320240	D	480234	E	480272	F	640480
	G	800480	H	1024600	I	320480	J	240320	K	800600	L	240400
	M	1024768	N	128128	P	1280800	Q	480800	R	640320	S	480128
	T	800320	U	8001280	V	176220	W	1280398	X	1024250	Y	1920720
	Z	800200	2	1024324	3	7201280	4	19201200	5	1366768	6	1280320
⑨	D: Digital L : LVDS M:MIPI											
⑩	Interface:											
	N	Without control board			A	8Bit		B	16Bit		H	HDMI
	I	I2C Interface			R	RS232		S	SPI Interface		U	USB
⑪	TS:											
	N	Without TS			T	Resistive touch panel			C	Capacitive touch panel (G-F-F)		
	G	Capacitive touch panel (G-G)					C1	Capacitive touch panel (G-F-F)+OCA				
	C2	Capacitive touch panel (G-F-F)+OCR					G1	Capacitive touch panel (G-G)+OCA				
	G2	Capacitive touch panel (G-G)+OCR					B	CTP+GG+USB				
⑫	Version: X:Raspberry pi											
⑬	Special Code		#:Fit in with ROHS directive regulations									

2.Summary

TFT 10.1" is a color active matrix TFT LCD using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 10.1 inch diagonally measured active area with WUXGA resolutions (1920 horizontal by 1200 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors.

3. General Specifications

Item	Dimension	Unit
Size	10.1	inch
Dot Matrix	1920(H) X RGB X 1200(V)	dots
Module dimension	257.96 x 168.6 x 12.33 Max	mm
Active area	216.8064(H) x 135.504(V)	mm
Pixel pitch	0.11292(H) x 0.11292(V)	mm
LCD type	TFT, Normally Black, Transmissive	
Interface	2ch-LVDS	
Driver IC	FL5893DA or equivalent	
Viewing Angle	80/80/80/80	
Aspect Ratio	16:10	
CTP IC	GT928 or Equivalent	
CTP Interface	I2C	
CTP FW Version	81	
CTP Resolution	1920 * 1200	
Backlight Type	LED, Normally White	
With /Without TP	With CTP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

4. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. □ 60°C, 90% RH MAX. Temp. > 60°C, Absolute humidity shall be less than 90% RH at 60°C

5. Electrical Characteristics

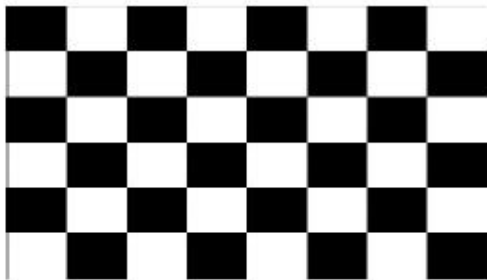
5.1. Typical Operation Conditions (At Ta = 25 °C,)

Item	Symbol	Min	Typ.	Max.	Unit	Unit
Power Supply Voltage	VDDIN	3.0	3.3	3.6	V	
	VRP	-	-	300	mV	Ripple
Power Supply Current	IDD	-	300	360	mA	Note 1
Power Consumption	PLCD	-	1	1.2	W	
Rush current	IRUSH	-	-	3.0	A	Note 2
Supply CTP	VDDT	3.1	3.3	3.5	V	
	IVDDT	—	73	110	mA	

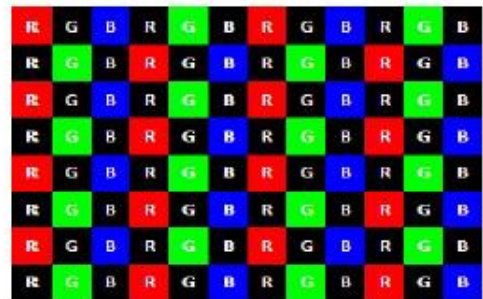
Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDDIN=3.3V, Frame rate fV=60Hz and Clock frequency = 80MHz. Test Pattern of power supply current

a) Typ : Mosaic 8 x 6 Pattern(L0/L255)



b) Max : skip subPixel(L255)

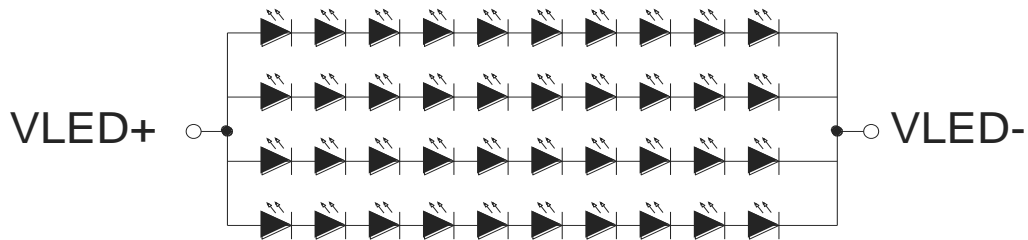


2. The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

5.2. Backlight Driving Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Supply voltage of white LED backlight	VL	26.0	29.0	32.0	V	Note 1
Current for LED backlight	IL	-	110	-	mA	
LED life time	-	50,000	-	-	Hr	Note2

Note 1 : There are 1 Groups LED



CIRCUIT DIAGRAM(LED 4*10=40 DIES)

Note 2 : $T_a = 25\text{ }^\circ\text{C}$

Note 3 : Brightness to be decreased to 50% of the initial value

Note 4 : The single LED lamp case

6. Interface timing Parameter and AC/DC Parameter

The 10.1" is operated by the DE only

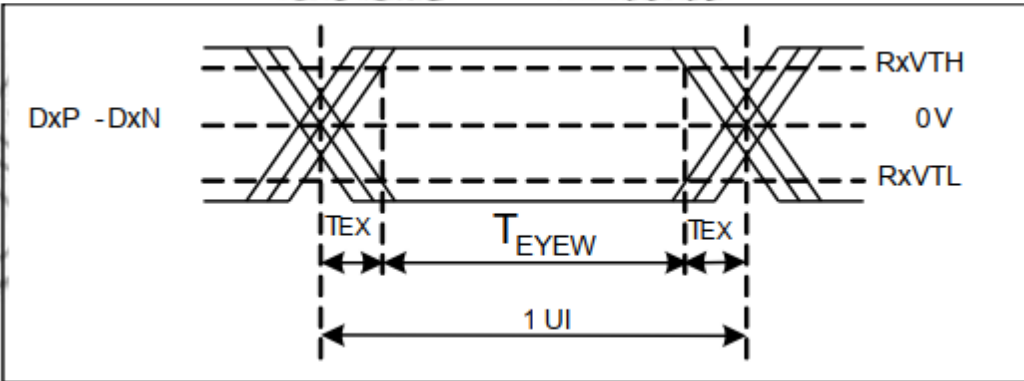
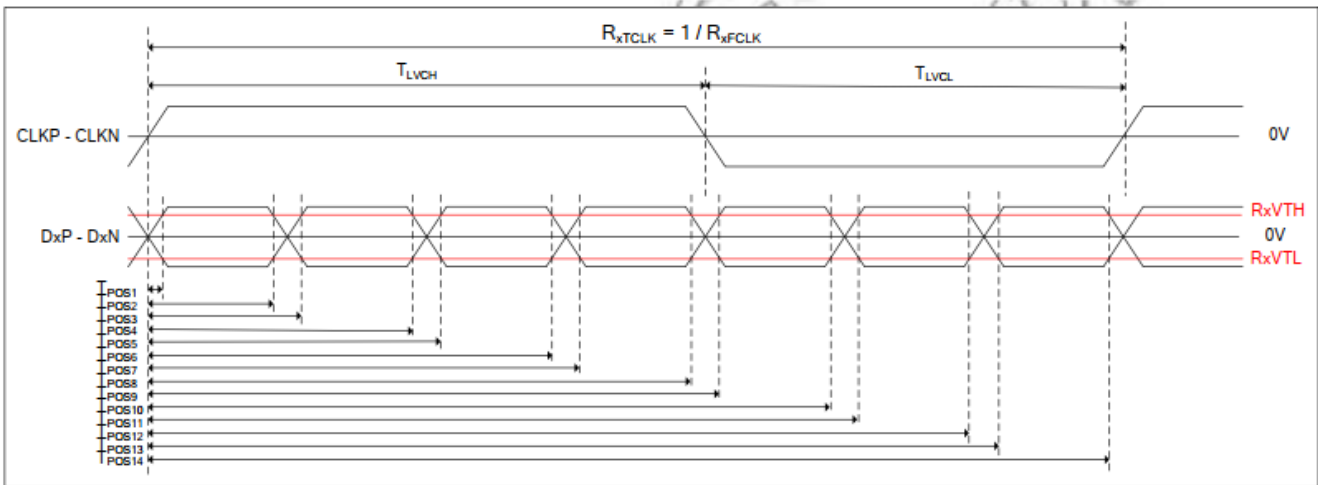
Table1. LVDS Timing Parameter

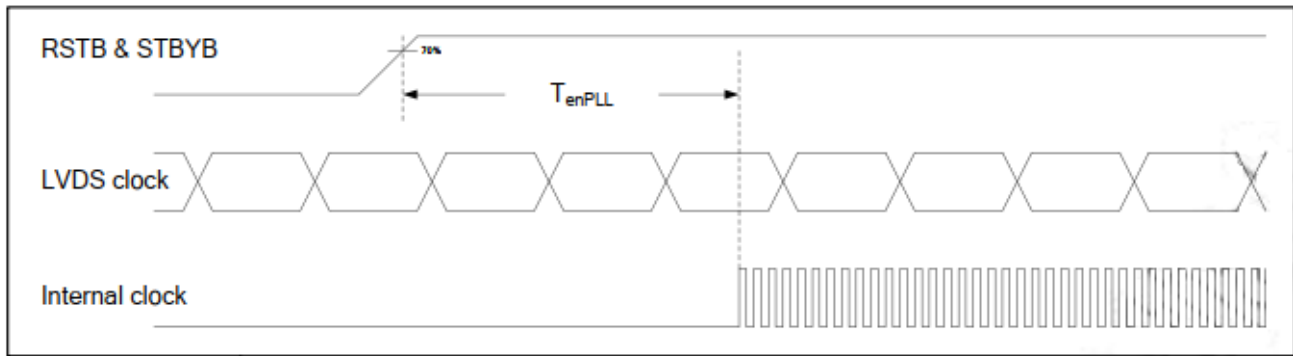
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK Frequency	Fdclk	74.5	77.56	85	MHz
Horizontal display area	Thd	960			DCLK
HSYNC period time	Th	989	1040	1248	DCLK
Horizontal Blank	THB	29	80	288	DCLK
HSYNC pulse width	Thp	2	10	255	DCLK
HSYNC back porch	thbp	3	6	255	DCLK
HSYNC Front porch	thfp	24	64	260	DCLK
Vertical display area	Tvd	1200			H
VSYNC period time	Tv	1243	1243	1560	H
Vertical Blank	TVB	43	43	360	H
VSYNC Pluse width	Tvp	4	4	20	H
VSYNC back porch	Tvbp	20	20	255	H
VSYNC front porch	Tvfp	19	19	260	H
Frequency	fV	-	60	-	Hz



<Table 2. LVDS AC Timing Specification>

Item	Signal	Symbol	Rating			Unit
			Min.	Typ.	Max.	
Clock Frequency	CLK	R_{XFCLK}	20	-	100	MHz
Clock Period		R_{XTCLK}	10	-	50	ns
1 data bit time		UI	-	1/7	-	R_{XTCLK}
Clock high time	CLK	T_{LVCH}		4		UI
Clock low time		T_{LVCL}		3		UI
Position 1	DATA	T_{POS1}	-0.25	0	0.25	UI
Position 2		T_{POS2}	0.75	-	1.25	
Position 3		T_{POS3}	0.75	1	1.25	
Position 4		T_{POS4}	1.75	-	2.25	
Position 5		T_{POS5}	1.75	2	2.25	
Position 6		T_{POS6}	2.75	-	3.25	
Position 7		T_{POS7}	2.75	3	3.25	
Position 8		T_{POS8}	3.75	-	4.25	
Position 9		T_{POS9}	3.75	4	4.25	
Position 10		T_{POS10}	4.75	-	5.25	
Position 11		T_{POS11}	4.75	5	5.25	
Position 12		T_{POS12}	5.75	-	6.25	
Position 13		T_{POS13}	5.75	6	6.25	
Position 14		T_{POS14}	6.75	-	7.25	
Input eye width		T_{EYEW}	0.5	-	-	
Input eye border		T_{EX}	-	-	0.25	
PLL wake-up time		T_{enPLL}	-	-	150	us



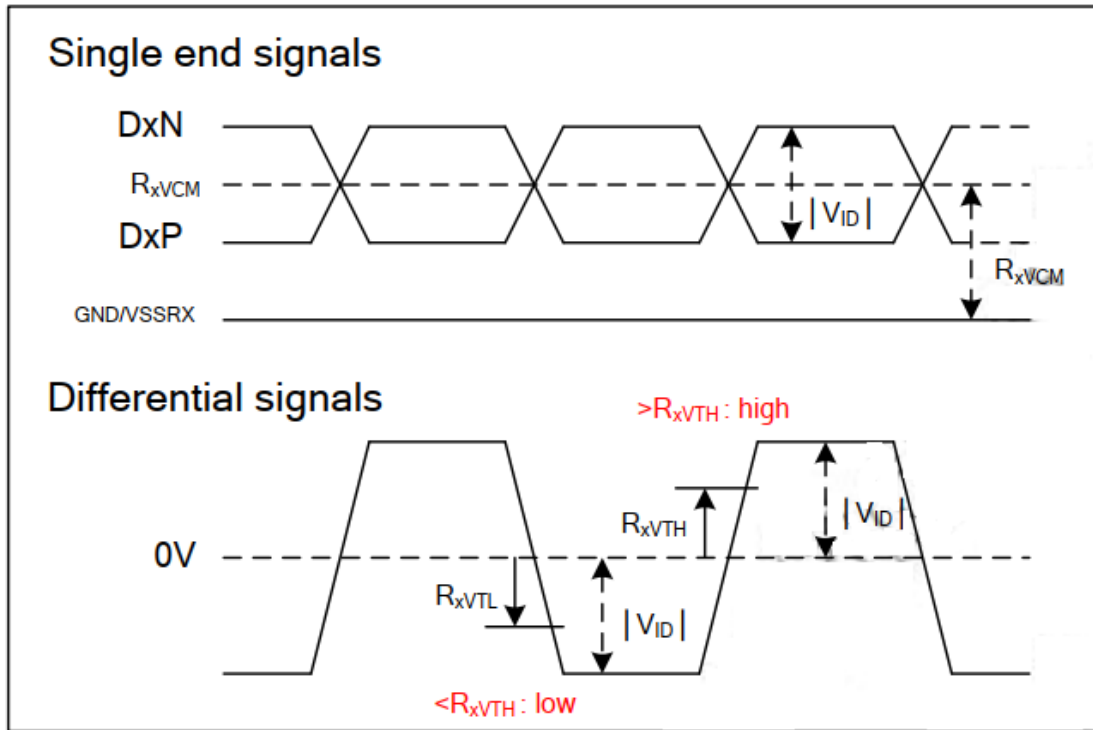


<Table 3. LVDS DC Timing Specification>

VSS1 = VSSRX = VSSP = 0V; Temp = Ta;

Item	Symbol	Condition	Rating			Unit	Applicable Pin
			Min.	Typ.	Max.		
Operating Voltage	VDDI	External Supply	3.0	3.3	3.6	V	VDDI
Operating Voltage	VDDR _X	External Supply	3.0	3.3	3.6	V	VDDR _X
Operating Voltage	VDDP	External Supply	3.0	3.3	3.6	V	VDDP
Operating Voltage	VDD_PFM	External Supply	3.0	3.3	3.6	V	VDD_PFM
Operating Voltage	VDD_OTP	External Supply	8.0	8.25	8.5	V	VDD_OTP
Operating Voltage	VSP	External Supply	5	-	6.4	V	VSP
Operating Voltage	VSN	External Supply	-6.4	-	-5	V	VSN
Operating Voltage	V15D	Built-In Power Supply		1.5		V	V15D
Operating Voltage	V15D_RX	Built-In Power Supply		1.5		V	V15D_RX
Operating Voltage	VRSP	Built-In Power Supply	4.5	-	6.0	V	VRSP
Operating Voltage	VRSN	Built-In Power Supply	-6.0	-	-4.5	V	VRSN
Operating Voltage	VRNL	Built-In Power Supply		-2.5		V	VRNL
Operating Voltage	VGMPHO	Built-In Power Supply	4.3	-	5.8	V	VGMPHO
Operating Voltage	VGMPMO	Built-In Power Supply	2.2	-	3.7	V	VGMPMO
Operating Voltage	VGMPLO	Built-In Power Supply	0.1	-	1.6	V	VGMPLO
Operating Voltage	VGMNHO	Built-In Power Supply	-5.8	-	-4.3	V	VGMNHO
Operating Voltage	VGMNMO	Built-In Power Supply	-3.7	-	-2.2	V	VGMNMO
Operating Voltage	VGMNLO	Built-In Power Supply	-1.6	-	-0.1	V	VGMNLO
Operating Voltage	VGH	Built-In Power Supply	7	-	22.5	V	VGH
Operating Voltage	VGL	Built-In Power Supply	-14.5	-	-7.5	V	VGL
Operating Voltage	VCOM	Built-In Power Supply	-1.80	-	0.75	V	VCOM
Input High-level Voltage	V _{IH}		0.8VDDI	—	VDDI	V	SPI Interface
Input Low-level Voltage	V _{IL}		VSS	—	0.2VDDI	V	SPI Interface
Output High-level Voltage	V _{OH}	VDDI=3.0V, IOL=1mA	0.8VDDI	—	VDDI	V	SPI Interface
Output Low-level Voltage	V _{OL}	VDDI=3.0V, IOL=1mA	VSS	—	0.2VDDI	V	SPI Interface
Input Leakage Current	I _{LI}	V _{IN} =VDDI or DGND	-1.0	—	1.0	μA	SPI Interface
Differential Input high threshold voltage	R _{LVTH}	R _{LVTH} =1.2V (Note1)			0.1	V	LVDS Interface
Differential Input low threshold voltage	R _{LVTL}		-0.1			V	LVDS Interface
Input voltage range (singled-end)	R _{LVIN}	(Note1)	0		VDD-1.0	V	LVDS Interface
Differential Input common mode voltage	R _{LVCM}	(Note1)	0.6	1.2	2.4 - V _{IO} /2	V	LVDS Interface
Differential Input voltage	V _{IO}	(Note1)	0.2	0.4	0.6	V	LVDS Interface
Differential Input leakage current	R _{V_{IO}}	(Note1)	-10		10	μA	LVDS Interface
Output Voltage Deviation	ΔV _S	Ta=25°C	—	TBD	TBD	mV	Source Pad

<Table 4. LVDS DC Timing Specification>

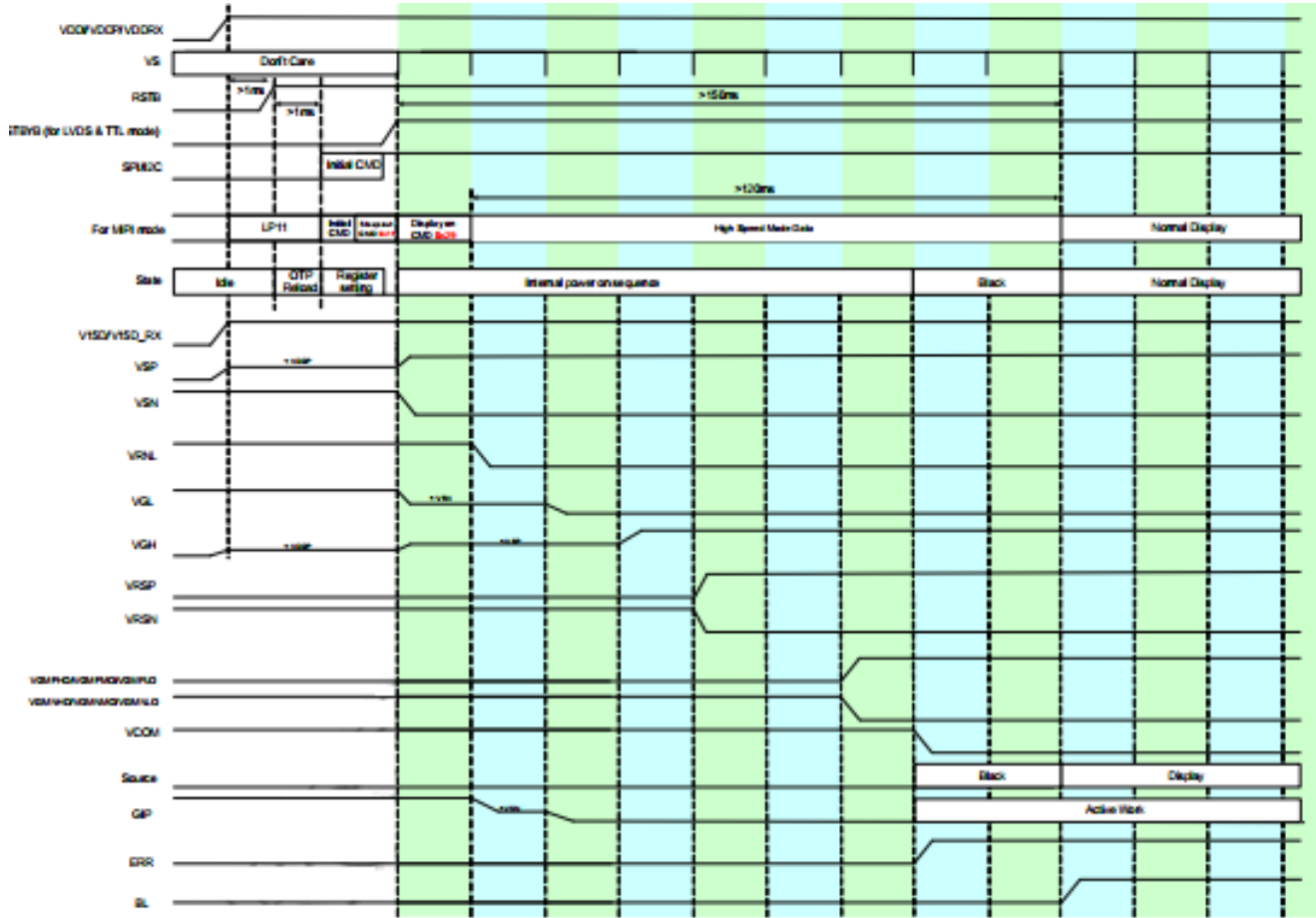


The current consumed by whole IC (bare die) with internal power system:

Item	Symbol	Condition	Rating			Unit
			Min.	Typ.	Max.	
Display Current (Digital)	IDDIO	TBD	-	TBD	-	μA
Display Current (Analog)	IDDP			TBD		
Standby	ISS	VDD_PFM=VDDRX=VDDP=VDDI=3.3V, Internal Power, During T_a temperature	-	TBD	-	μA

Note: The current is DC characteristic of a "Bare Chip".

7. Power Sequence



8. Optical Characteristics

Item	Symbol	Condition.	Min	Typ.	Max.	Unit	Remark	
Response time	Tr+Tf	$\theta=0^\circ$ 、 $\Phi=0^\circ$	-	30	35	.ms	Note 3	
Contrast ratio	CR	At optimized viewing angle	700	900	-	-	Note 4	
Color Chromaticity	White	Wx	$\theta=0^\circ$ 、 $\Phi=0$	0.235	0.285	0.335	-	Note 2,6,7
		Wy		0.277	0.327	0.377	-	
Viewing angle	Hor.	Θ_R	$CR \geq 10$	70	80	-	Deg.	Note 1
		Θ_L		70	80	-		
	Ver.	Φ_T		70	80	-		
		Φ_B		70	80	-		
Brightness	-	-	300	400	-	cd/m ²	Center of display	
Uniformity	(U)	-	70	-	-	%	Note 5	
NTSC Ratio	NTSC	-	-	72	-	%	Note 8	

Ta=25±2°C

Note 1: Definition of viewing angle range

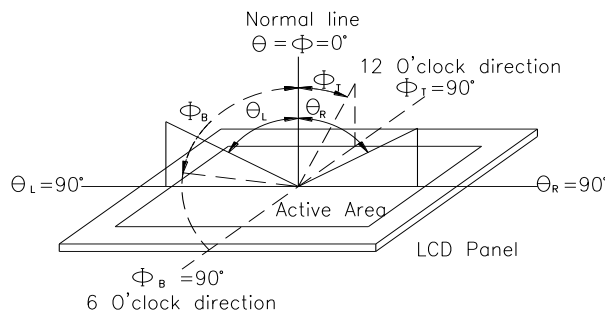


Fig. 8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

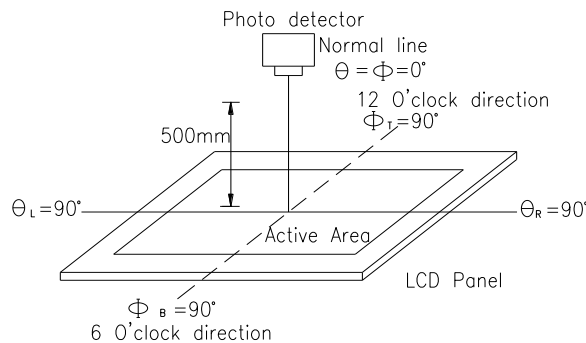
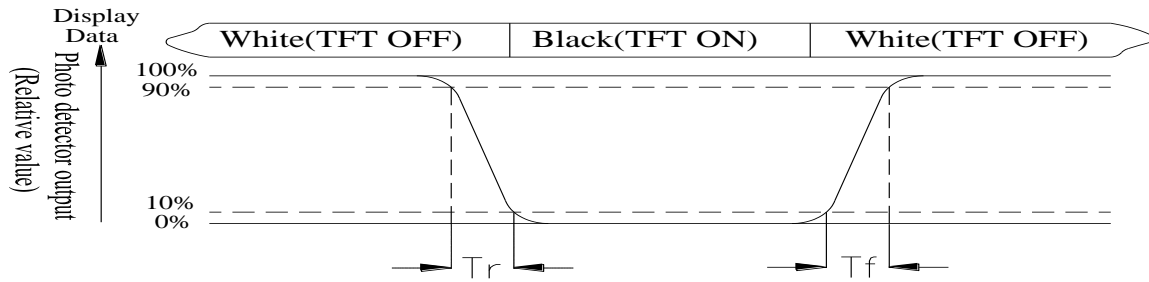


Fig. 8.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = \text{Lmin/Lmax} \times 100\%$$

L = Active area length

W = Active area width

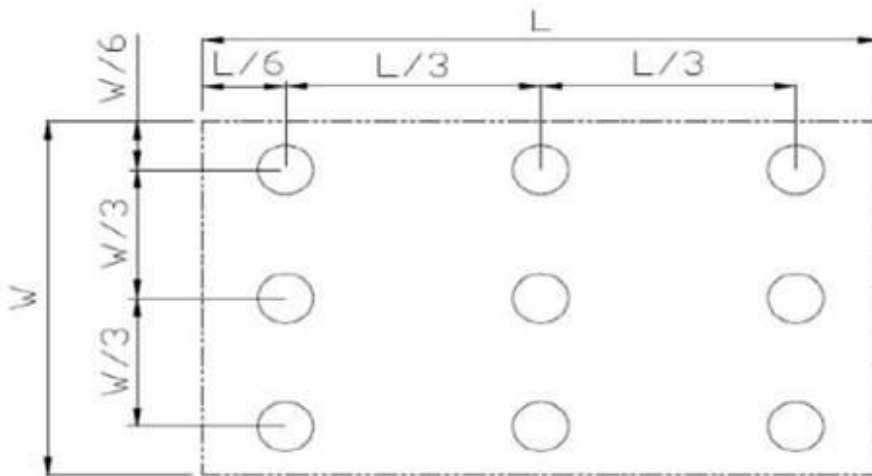


Fig 8.3. Definition of uniformity

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

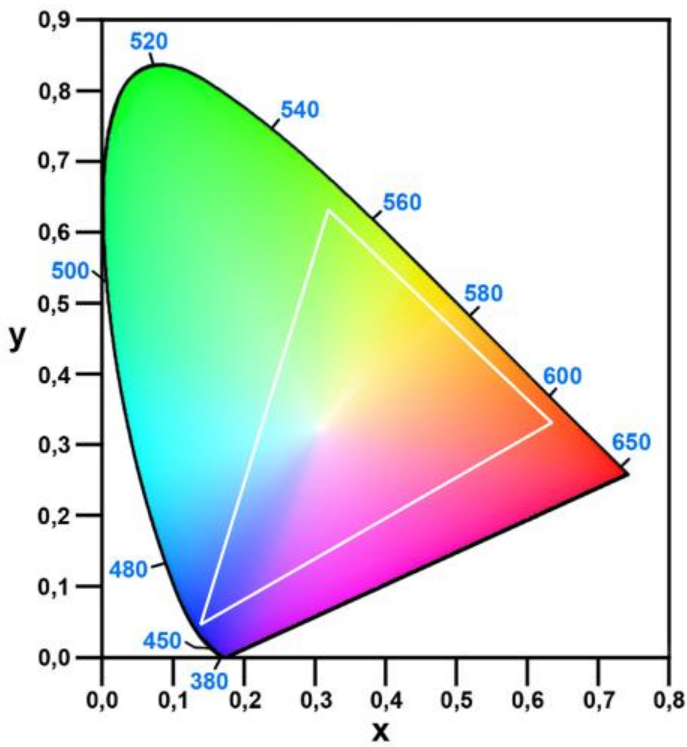
Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 8: Definition of Color of CIE Coordinate and NTSC Ratio

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

NTSC Ratio= 74.48%

	x	y
R	0.644	0.344
G	0.315	0.632
B	0.157	0.054



9.Interface

9.1. TFT LCD MODULE

Pin No.	Symbol	Description
1-5	NC	No connection
6	GND	Ground
7	ELV3P	EVEN LVDS Positive data signal (+)
8	ELV3N	EVEN LVDS Negative data signal (-)
9	GND	Ground
10	ELV2P	EVEN LVDS Positive data signal (+)
11	ELV2N	EVEN LVDS Negative data signal (-)
12	GND	Ground
13	ELVCLKP	EVEN LVDS Positive CLK signal (+)
14	ELVCLKN	EVEN LVDS Negative CLK signal (-)
15	GND	Ground
16	ELV1P	EVEN LVDS Positive data signal (+)
17	ELV1N	EVEN LVDS Negative data signal (-)
18	GND	Ground
19	ELV0P	EVEN LVDS Positive data signal (+)
20	ELV0N	EVEN LVDS Negative data signal (-)
21	GND	Ground
22	OLV3P	Odd LVDS Positive data signal (+)
23	OLV3N	Odd LVDS Negative data signal (-)
24	GND	Ground
25	OLV2P	Odd LVDS Positive data signal (+)
26	OLV2N	Odd LVDS Negative data signal (-)
27	GND	Ground
28	OLVCLKP	Odd LVDS Positive CLK signal (+)
29	OLVCLKN	Odd LVDS Negative CLK signal (-)
30	GND	Ground
31	OLV1P	Odd LVDS Positive data signal (+)
32	OLV1N	Odd LVDS Negative data signal (-)
33	GND	Ground
34	OLV0P	Odd LVDS Positive data signal (+)

35	OLV0N	Odd LVDS Negative data signal (-)
36	GND	Ground
37 -39	NC	No connection (PIN39 and PIN40 cannot be short-circuited together)
40	NC	No connection alone
41-45	VDDIN	Power supply VDDIN=3.3V (Typ.)

9.2. Backlight PIN Definition

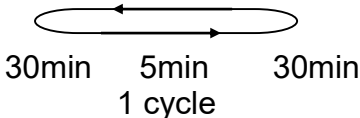
Pin No.	Symbol	Description
1	VLED+	Red, LED_ Anode
2	VLED-	Black, LED_ Cathode

9.3. CTP PIN Definition

Pin	Symbol	Function	Remark
1	SDA	I2C data input and output	
2	SCL	I2C clock input	
3	RST	External Reset, Low is active	
4	INT	External interrupt to the host	
5	VDDT	Power Supply : +3.3V	
6	VSS	Ground for analog circuit	

10. Reliability

Content of Reliability Test (Wide temperature, -20°C~70°C)

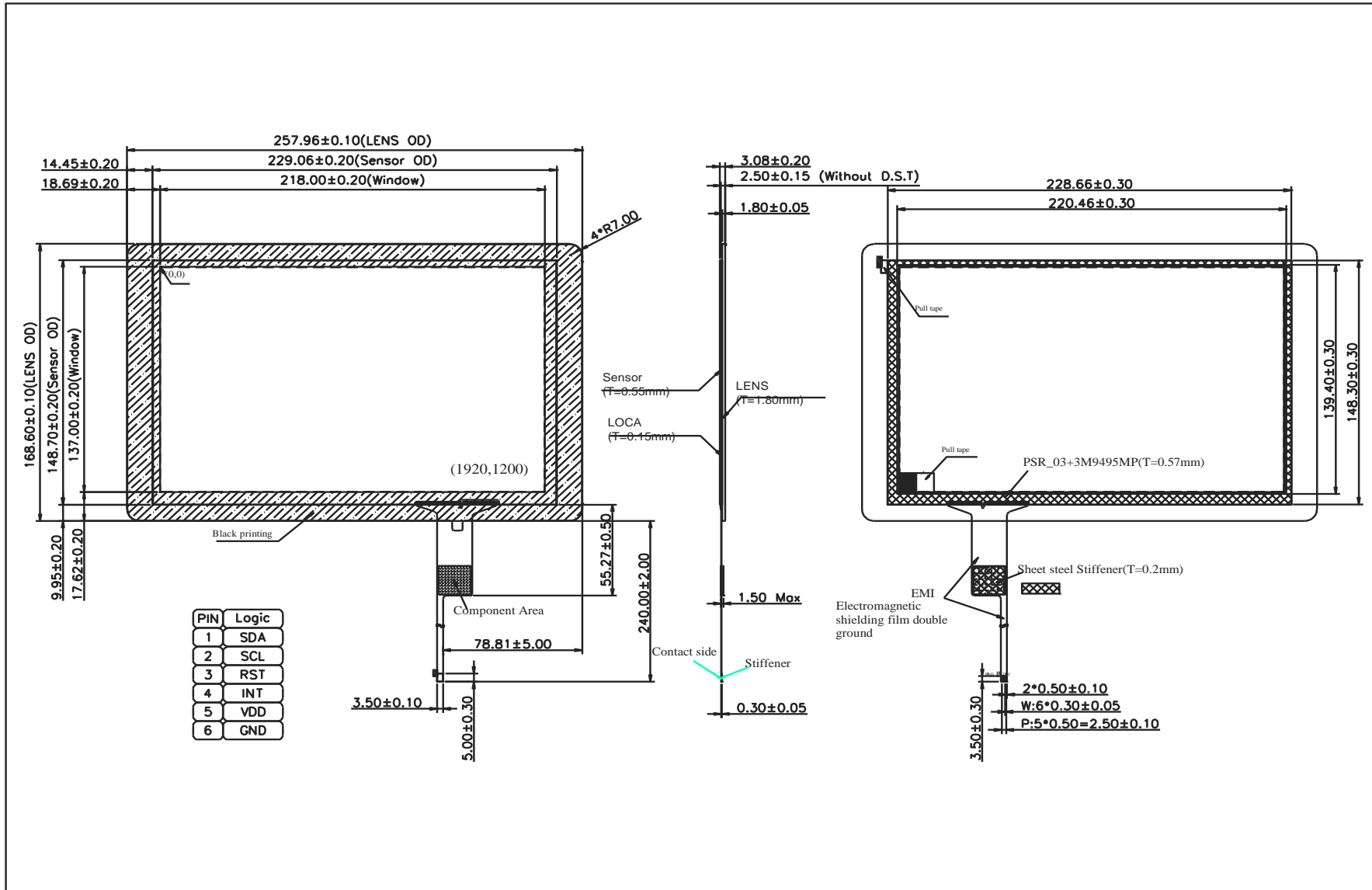
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max	60°C,90%RH 96hrs	1,2
Thermal shock resistance	<p>The sample should be allowed stand the following 10 cycles of operation</p> <p style="text-align: center;">-20°C 25°C 70°C</p>  <p style="text-align: center;">30min 5min 30min</p> <p style="text-align: center;">1 cycle</p>	-20°C/70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800V(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

11.Touch Panel Information



11.1. Timing for Read Operation

First, set address pointer based on the aforesaid Write Operation timing sequence. Then, resend

Start condition to perform Read addressing and read data in the registers.



Address_R: Slave address with Read control bit.

NACK: Host issues NACK after reading the last byte.

After setting Read addresses, the host can read one or more than one byte at a time.

GT927/GT928/GT9110 will automatically increase the address pointer and send subsequent data in sequence.

The Stop condition (the first E signal as shown in the above diagram) after setting the address pointer is optional. However, the repeated Start condition has to be sent.

11.2. Coordinate registers

Addr	Access	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
0x8140	R	Product ID(first Byte, ASCII)							
0x8141	R	Product ID(second Byte, ASCII)							
0x8142	R	Product ID(third Byte, ASCII)							
0x8143	R	Product ID(forth Byte, ASCII)							
0x8144	R	Firmware version(HEX.low byte)							
0x8145	R	Firmware version (HEX.high byte)							
0x8146	R	x coordinate resolution (low byte)							
0x8147	R	x coordinate resolution (high byte)							
0x8148	R	y coordinate resolution (low byte)							
0x8149	R	y coordinate resolution (high byte)							

0x814A	R	Vendor_id(ID of the current module)				
0x814B	R	Reserved				
0x814C	R	Reserved				
0x814D	R	Reserved				
0x814E	R/W	buffer status	large detect	Reserved	HaveKey	number of touch points
0x814F	R	track id as 32 indicates HotKnot proximity detection signal				
0x8150	R	PxyOk	Reserved			
0x8151	R	PxyOk	Reserved			
0x8152	R	Reserved				
0x8153	R	Reserved				
0x8154	R	Reserved				
0x8155	R	Reserved				
0x8156	R	Reserved				
0x8157	R	pen_sta	Reserved			track id
0x8158	R	point 1 x coordinate (low byte)				
0x8159	R	point 1 x coordinate (high byte)				
0x815A	R	point 1 y coordinate (low byte)				
0x815B	R	point 1 y coordinate (high byte)				
0x815C	R	Point 1 size (low byte)				
0x815D	R	point 1 size (high byte)				
0x815E	R	Reserved				
0x815F	R	track id				
0x8160	R	point 2 x coordinate (low byte)				
0x8161	R	point 2 x coordinate (high byte)				
0x8162	R	point 2 y coordinate (low byte)				
0x8163	R	point 2 y coordinate (high byte)				
0x8164	R	point 2 size (low byte)				
0x8165	R	point 2 size (high byte)				
0x8166	R	Reserved				
0x8167	R	track id				
0x8168	R	point 3 x coordinate (low byte)				
0x8169	R	point 3 x coordinate (high byte)				
0x816A	R	point 3 y coordinate (low byte)				
0x816B	R	point 3 y coordinate (high byte)				
0x816C	R	point 3 size (low byte)				
0x816D	R	point 3 size (high byte)				
0x816F	R	track id				
0x8170	R	point 4 x coordinate (low byte)				
0x8171	R	point 4 x coordinate (high byte)				
0x8172	R	point 4 y coordinate (low byte)				

0x8173	R	point 4 y coordinate (high byte)
0x8174	R	point 4 size (low byte)
0x8175	R	point 4 size (high byte)
0x8176	R	Reserved
0x8177	R	track id
0x8178	R	point 5 x coordinate (low byte)
0x8179	R	point 5 x coordinate (high byte)
0x817A	R	point 5 y coordinate (low byte)
0x817B	R	point 5 y coordinate (high byte)
0x817C	R	point 5 size (low byte)
0x817D	R	point 5 size (high byte)
0x817E	R	Reserved
0x817F	R	track id
0x8180	R	point 6 x coordinate (low byte)
0x8181	R	point 6 x coordinate (high byte)
0x8182	R	point 6 y coordinate (low byte)
0x8183	R	point 6 y coordinate (high byte)
0x8184	R	point 6 size (low byte)
0x8185	R	point 6 size (high byte)
0x8186	R	Reserved
0x8187	R	track id
0x8188	R	point 7 x coordinate (low byte)
0x8189	R	point 7 x coordinate (high byte)
0x818A	R	point 7 y coordinate (low byte)
0x818B	R	point 7 y coordinate (high byte)
0x818C	R	point 7 size (low byte)
0x818D	R	point 7 size (high byte)
0x818E	R	Reserved
0x818F	R	track id
0x8190	R	point 8 x coordinate (low byte)
0x8191	R	point 8 x coordinate (high byte)
0x8192	R	point 8 y coordinate (low byte)
0x8193	R	point 8 y coordinate (high byte)
0x8194	R	point 8 size (low byte)
0x8195	R	point 8 size (high byte)
0x8196	R	Reserved
0x8197	R	track id
0x8198	R	point 9 x coordinate (low byte)
0x8199	R	point 9 x coordinate (high byte)
0x819A	R	point 9 y coordinate (low byte)
0x819B	R	point 9 y coordinate (high byte)

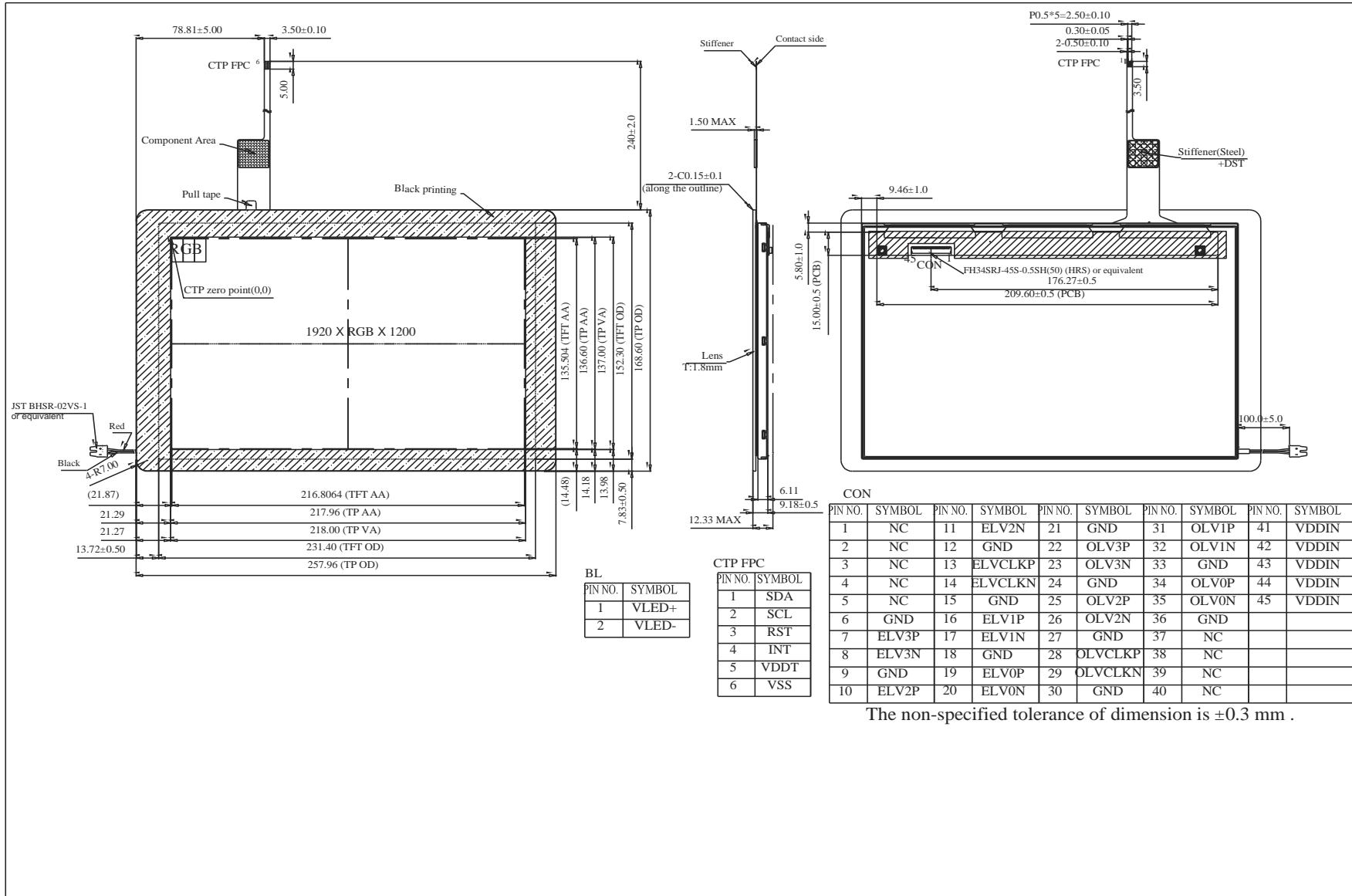
0x819C	R	point 9 size (low byte)
0x819D	R	point 9 size (high byte)
0x819E	R	Reserved
0x819F	R	track id
0x81A0	R	point 10 x coordinate (low byte)
0x81A1	R	point 10 x coordinate (high byte)
0x81A2	R	point 10 y coordinate (low byte)
0x81A3	R	point 10 y coordinate (high byte)
0x81A4	R	point 10 size (low byte)
0x81A5	R	point 10 size (high byte)
0x81A6	R	Reserved
0x81A7	R	KeyValue

11.3. I2C protocol

3.3V ,400BPS ,pull high 2K ohm

Parameter	Symbol	Min.	Max.	Unit
SCL low period	t_{lo}	0.9	-	us
SCL high period	t_{hi}	0.8	-	us
SCL setup time for START condition	t_{st1}	0.4	-	us
SCL setup time for STOP condition	t_{st3}	0.4	-	us
SCL hold time for START condition	t_{hd1}	0.3	-	us
SDA setup time	t_{st2}	0.4	-	us
SDA hold time	t_{hd2}	0.4	-	us

12. Contour Drawing





1、Panel Specification :

- 1. Panel Type : Pass NG , _____
- 2. View Direction : Pass NG , _____
- 3. Numbers of Dots : Pass NG , _____
- 4. View Area : Pass NG , _____
- 5. Active Area : Pass NG , _____
- 6. Operating : Pass NG , _____
- 7. Storage Temperature : Pass NG , _____
- 8. Others : _____

2、Mechanical

- 1. PCB Size : Pass NG , _____
- 2. Frame Size : Pass NG , _____
- 3. Material of Frame : Pass NG , _____
- 4. Connector Position : Pass NG , _____
- 5. Fix Hole Position : Pass NG , _____
- 6. Backlight Position : Pass NG , _____
- 7. Thickness of PCB : Pass NG , _____
- 8. Height of Frame to PCB : Pass NG , _____
- 9. Height of Module : Pass NG , _____
- 10. Others : Pass NG , _____

3、Relative Hole Size :

- 1. Pitch of Connector : Pass NG , _____
- 2. Hole size of Connector : Pass NG , _____
- 3. Mounting Hole size : Pass NG , _____
- 4. Mounting Hole Type : Pass NG , _____
- 5. Others : Pass NG , _____

4、Backlight Specification :

- 1. B/L Type : Pass NG , _____
- 2. B/L Color : Pass NG , _____
- 3. B/L Driving Voltage (Reference for LED) : Pass NG , _____
- 4. B/L Driving Current : Pass NG , _____
- 5. Brightness of B/L : Pass NG , _____
- 6. B/L Solder Method : Pass NG , _____
- 7. Others : Pass NG , _____

>> **Go to page 2** <<



Winstar Module Number : _____

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5、Electronic Characteristics of Module :

- 1. Input Voltage : Pass NG , _____
- 2. Supply Current : Pass NG , _____
- 3. Driving Voltage for LCD : Pass NG , _____
- 4. Contrast for LCD : Pass NG , _____
- 5. B/L Driving Method : Pass NG , _____
- 6. Negative Voltage Output : Pass NG , _____
- 7. Interface Function : Pass NG , _____
- 8. LCD Uniformity : Pass NG , _____
- 9. ESD test : Pass NG , _____
- 10. Others : Pass NG , _____

6、Summary :

Sales signature : _____

Customer Signature : _____

Date : / / _____