



ePoster T-CON Board EJ8951E-1 Product Specification

Issue Date : 2023/2/8



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Record of Revision

Version	Issue Date	Page	Description
1.1	2023/2/8	10	Power supply value is modified
1.0	2020/9/18		Final specification released

1. General Description

1.1. Introduction

EJ8951EL-1W is designed for driving Electrical Paper Display with the following features:

- Support EPD panel modules:
 - EINK EL133US1
 - EINK ED133UT2
 - EINK ES133UT2
 - EINK EL097TR1
- Two Adjustable LDOs for Source Driver Supply
 - VPOS: +15V, 200mA at VIN= 5V
 - VNEG: -15V, 200mA at VIN= 5V
- Adjustable VCOM Driver for Accurate Panel Backplane Biasing 0V to -5.11V
- Two Adjustable LDOs for Gate Driver Supply
 - VGH: +27V, 15mA at VIN= 5V
 - VGL: -20V, 15mA at VIN= 5V
- External IO
 - Micro USB x 1 / USB (Wafer) x 1
 - SPI x 1
 - DC-in 5V x 1
- Internal IO
 - Panel Out x 1
 - Rotary Coded Switch (10 Steps) x 1
- Working Temperature: -15°C-70°C
- Dimension: 180(L)x45(H)mm
- Weight: 25g



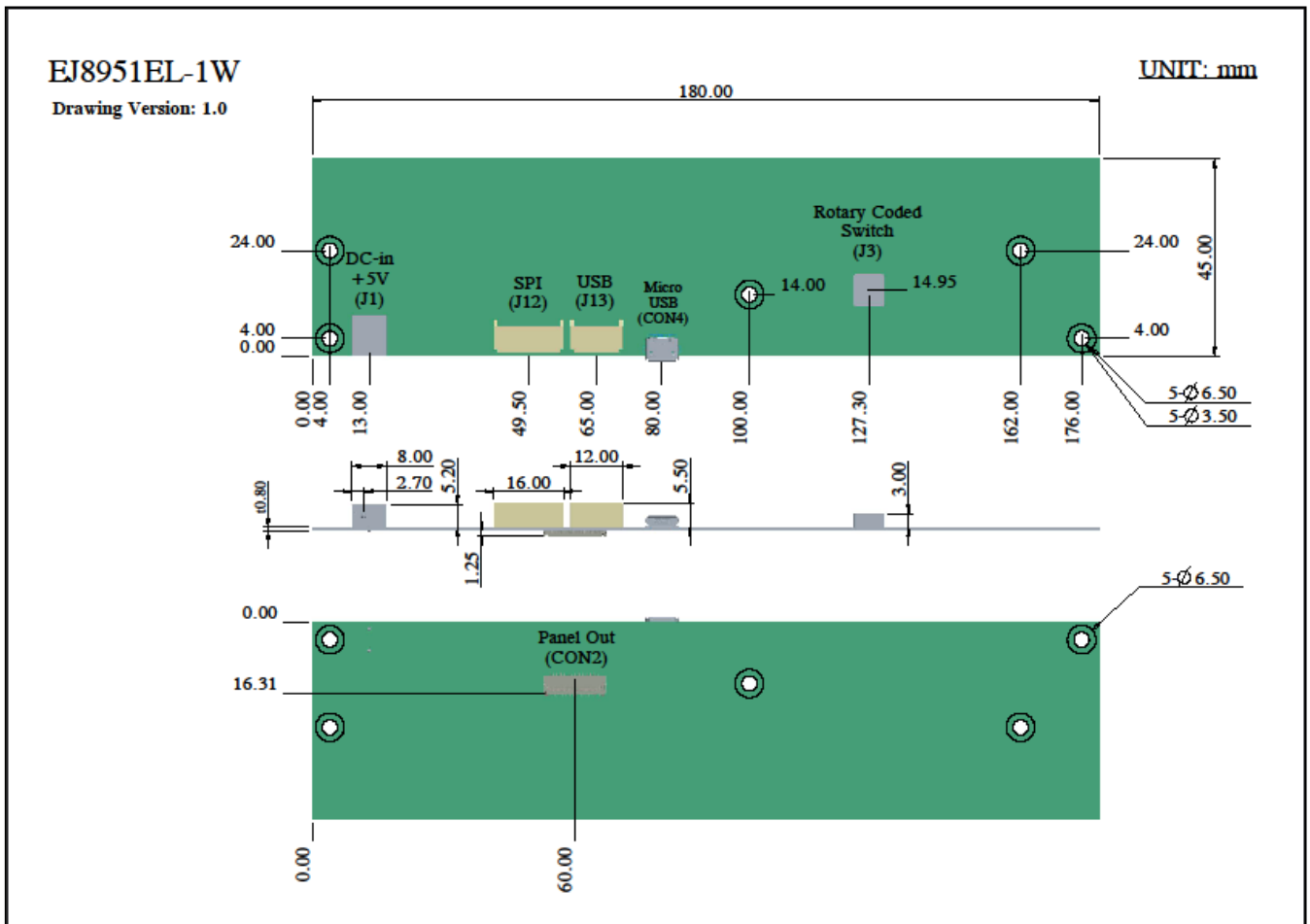
1.2. Electrical Characteristics

ITEM	SYMBOL	MIN.	MAX.	I
Power Supply	Vs	4.5 V	5.5 V	
Panel Power	VPOS	+14.25 V	+15.0 V	200 mA
	VNEG	-15.0 V	-14.25 V	200 mA
	VCOM	-5.11 V	0.0 V	15 mA
	VGH	+26.0 V	+28.0 V	15 mA
	VGL	-21.0 V	-19.0 V	15 mA

1.3. Power Consumption

ITEM	MIN.	MAX.	UNIT
TCON Board Only		5.0	W
Standby		0.8	W

2. Mechanical Drawing



3. Electrical Definition

3.1. Panel Out (CON2): JST 39FXL-RSM1-S-H-TB or compatible

PIN	SIGNAL	DESCRIPTION
1	VNEG	Negative power supply source driver
2	VPOS	Positive power supply source driver
3	VSS	Ground
4	VDD	Digital power supply drivers
5	SCLK	Clock source driver
6	LE	Latch enable source driver
7	OE	Output enable source driver
8	VSS	Ground
9	VSS	Ground
10	-	No Connection
11	STL	Start pulse source driver
12	D0	Data signal source driver
13	D1	Data signal source driver
14	D2	Data signal source driver
15	D3	Data signal source driver
16	D4	Data signal source driver
17	D5	Data signal source driver
18	D6	Data signal source driver
19	D7	Data signal source driver
20	VSS	Ground
21	-	No Connection
22	VCOM	Common connection
23	VGH	Positive power supply gate driver
24	VGL	Negative power supply gate driver
25	-	No Connection
26	-	No Connection
27	-	No Connection
28	MODE1	Output mode selection gate driver
29	VSS	Ground
30	VSS	Ground
31	VSS	Ground



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32	STL	Start pulse gate driver
33	CKV	Clock gate driver
34	BORDER	Border connection
35	VSS	Ground
36	VSS	Ground
37	VSS	Ground
38	VSS	Ground
39	VSS	Ground

3.2. Micro USB (CON4): MicroUSB Jack

PIN	SIGNAL
1	+5V
2	D+
3	D-
4	GND

3.3. USB (J13): Wafer 4-Pin Pitch=2.0mm

PIN	SIGNAL
1	+5V
2	D+
3	D-
4	GND

3.4. SPI (J12): Wafer 6-Pin Pitch=2.0mm

PIN	SIGNAL
1	HRDY
2	MISO
3	MOSI
4	SCK
5	CS
6	GND

3.5. DC-IN 5V (J1): DC Jack 3.5mm/1.3mm

PIN	SIGNAL
1	+5V
2	GND



4. Packing Information

4.1. Definition of Label

OOOOO YYWW XXX

OOOOO: Simplified Part Number

YYWW: Production Year/Week

XXX: Serial Number